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Examiner Signature	/Zheng Wei/	Date Considered	08/30/2006
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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<i>Complete if Known</i>	
				Application Number	10/748,384
Sheet      2      of      2				Filing Date	December 29, 2003
				First Named Inventor	Tatiana Shpeisman
				Art Unit	
				Examiner Name	
				Attorney Docket Number	42P17251

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
ZW		KO M-Y, BHATTACHARYYA S S: "Partitioning for DSP Software Synthesis", Software and Compilers for Embedded Systems: 7th International Workshop, Scopes 2003, Vienna, Sept. 24-26, 2003. Proceedings (Springer LNCS 2826), pgs. 344-358, XP002349106.	
ZW		YU HEN HU (Editor): "Programmable Digital Signal Processors", 2001 Marcel Dekker, XP002352402, ISBN: 0-8247-0647-1, Chap. 8 - S S Bhattacharyya: Hardware/Software Coynthesis of DSP Systems (pgs 333-378).	
ZW		BHATTACHARYYA S S ET AL: "Software Synthesis and Code Generation for Signal Processing Systems" IEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, vol. 47, no. 9, Sept 2000, pgs 849-875, XP002352333.	
ZW		SAFARI ET AL: "Testability improvement during high-level synthesis", 12th Asian Test Symposium, 2003. ATS 2003. Nov. 16-19, 2003, Piscataway, NJ, USA, IEEE, Nov. 19, 2003, page 505, XP010672538.	

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Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.  
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